



MODULAR DEVICES, INC.

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An ISO 9001:2015 Registered Company

BIT Signal

The bit signal is connected to pin 1. This signal is referenced to the input return (through the negative leg of the EMI filter). The BIT signal gives an opportunity to sense from the input side what the output is doing. The normal voltage on the BIT line is 5.5VDC \pm 1VDC. When the output is inhibited, or cycling in overcurrent, or cycling in overvoltage conditions, the BIT line goes low. In this state, the output is between 0 and 2VDC.

It is important to note that the BIT signal is referenced to the input return through the bottom leg of the input filter. The BIT line should not be allowed to sink more than 1mA from an external source when in the low state. When in the high state, the voltage is clamped by a Zener diode. No more than 1mA should be sourced into the BIT line when in the high state.



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